Express Mail No. EV355037520US **PATENT**

IBM/174DV1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Michael Borkenhagen et al.

Art Unit: Unknown Examiner: Unknown

Serial No.: FILED HEREWITH

Atty. Docket No.: IBM/174DV1

Filed:

September 12, 2003

For:

DATA STROBE GATING FOR SOURCE SYNCHRONOUS

COMMUNICATIONS INTERFACE

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure under the provisions of 37 C.F.R. § 1.56, the references listed on the enclosed Form PTO-1449 are brought to the attention of the Examiner. Each of the references was cited in the parent for the above-identified application, U.S. Serial No.09/836,651, filed on April 17, 2001. Accordingly, no copies of the references are provided.

The Examiner will also note that this application may also be related to co-pending U.S. Patent Application Serial No. 09/617,558 filed on July 17, 2000 by Borkenhagen et al.

No representation is made that any of the references are "prior art" within the meaning of 35 U.S.C. § 102 or § 103. Moreover, any explanations herein are not to be taken as a representation that the references have been thoroughly reviewed. In particular, no representation as to the relative relevance of any portion of a reference is intended.

Consideration of the references and the remarks presented herein in the above-identified application is respectfully requested.

If there are any questions regarding this paper, or which might otherwise further this case onto allowance, please contact the undersigned at (513) 241-2324. Moreover, if any other charges or credits are necessary to complete this communication, please apply them to Deposit Account 23-3000.

12 SEP 2003

Date

Respectfully submitted,

Scott A. Stinebruner

Reg. No. 38,323

WOOD, HERRON & EVANS, L.L.P.

2700 Carew Tower

441 Vine Street

Cincinnati, Ohio 45202

(513) 241-2324

Express Mail N	o. <u>EV355</u>	037	5 <u>20</u> 1	<u>JS</u>										She	eet 1 (A) of 1
SUBSTITUTE (MODIFIED)	-144	9					.S. DEPARTM ATENT AND		ATTY. DOCKET NO.			SERIAL NO. FILED HEREWITH			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT												APPLICANT John Michael Borkenhagen et al.			
(37 CFR 1.98		(Us	e se	evera	al sl	neet	s if n	ecessary)			FILING DATE September 12, 2003			GROUP	
									U.S.	PATENT (DOCUMENTS	•			
EXAMINER INITIAL		PATENT NUMBER							ISSUE DATE		PATENTEE		CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.A	6	1	4	7	9	2	7	11/14/2000	Ooishi			365	233	6/3/1999
	A.B	6	2	1	9	7	4	6	4/17/2001	Vogley			711	104	12/6/1999
	A.C	6	3	1	6	9	8	0	11/13/2001	Vogt et	al.		327	273	6/30/2000
	A.D	6	4	5	6	5	4	4	9/24/2002	Zumkeh	nr		365	193	3/30/2001
	A.E														
	A.F														
	A.G		L												
	A.H		L			L	L								
	A.1	1_	_	L											
	A.J		_	L		_	L								
	A.K		L		_	L	L		<u></u>		<u>-</u>		L	L	
	,				FC	RE	IGN	PAT	ENTS OR PU	BLISHED	FOREIGN PA	TENT A	PPLICATION	IS .	
		DOCUMENT NUMBER						P	UBLICATION DATE		COUNTRY OF		CLASS	SUBCLASS	TRANSLATION (YES/NO)
	A.L							<u> </u>		_					
	A.M														
	A.N							_							
	A.O							<u> </u>							
	A.P														
	A.Q														
<u> </u>	τ				0	TH	ER	DOC	UMENTS (Incl	luding Auti	hor, Title, Date	e, Piace o	of Publication)	
	A.R							DR) \$		fication", J	IEDEC Standa	<u>ırd,</u> JESC	79, JEDEC	Solid State Tech	nology Association
	A.S	U.S. Patent Application Serial No. 09/617,558, entitled "Programmable Compensated Delay for Difiled on 7/17/2000 by Borkenhagen et al. (ROC920000157US1).													DRAM Interface,"
	A.T														
EXAMINER											DATE CONSIDERED				
									r not in conform n to applicant.		raw line throug	gh citation	n only if not i	n conformance <u>a</u>	nd not considered.